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PATENT APPLICATION
Docket No.: 2037.1002-003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richard C. Foss, Peter B. Gillingham, Robert
Harland, Masami Mitsuhashi and Atsushi Wada

Application No.: 10/032,431 Group: Not Assigned Yet

Filed: December 21, 2001 Examiner: Not Assigned Yet

For: DYNAMIC RANDOM ACCESS MEMORY USING
IMPERFECT ISOLATING TRANSISTORS

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Assistant Commissioner for Patents, P.O. Box 2327, Washington, D.C. 20231 Arlington, VA 22202	
on 5/20/02	Rachel Cohen
Date	Signature
Rachel Cohen	
Typed or printed name of person signing certificate	

WRITTEN CONSENT OF ASSIGNEE IN CONTINUATION
OF REISSUE APPLICATION

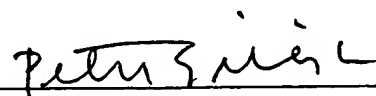
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Mosaid Technologies Incorporated is the assignee of the referenced patent application
and does hereby consent to the referenced continuation of reissue patent application.

A Certificate under 37 C.F.R. §3.73(b) certifying that, to the best of my knowledge and
belief, title is in Mosaid Technologies Incorporated is being filed concurrently with this Written
Consent.

Respectfully submitted,


Peter Gillingham
Vice President Business Development
Mosaid Technologies Incorporated
11 Hines Road
Kanata, Canada K2K 2X1

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JMS/GMB/dar
December 18, 2001



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Date	Signature
<u>Rachel Cohen</u>	
Typed or printed name of person signing certificate	

DECLARATION FOR CONTINUATION OF
REISSUE PATENT APPLICATION

The Assistant Commissioner
for Patents
Washington, DC 20231

Sir:

As a below named inventor of U.S. Patent No. 5,414,662, I hereby declare that:

1. My residence, post office address and citizenship are stated below next to my name.
2. I believe that the named inventors are the original and first inventors of the claimed subject matter described in United States Patent No. 5,414,662 granted

on May 9, 1995 (hereinafter the '662 patent) entitled DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT ISOLATING TRANSISTORS for which a reissue patent is sought.

3. I have reviewed and I understand the contents of the application, including the claims, which was filed on December 21, 2000 as U.S. Application No. 10/032,431.
4. I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information known by me to be material to patentability as defined in 37 C.F.R. 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing date)	(Status: patented, pending, abandoned)
<u>08/853,507</u>	<u>May 8, 1997</u>	<u>Pending (Allowed)</u>

5. I acknowledge the duty to disclose information that is known by me to be material to patentability as defined in 37 C.F.R. § 1.56.
6. I believe the '662 patent to be wholly or partially inoperative or invalid by reason of claiming, without any deceptive intention, more than we had the right to claim in the patent in one respect and less than we had the right to claim in the patent in another respect.
7. One error being relied upon as a basis for reissue is that the original claims 1-17 failed to claim an embodiment in which the high voltage supply V_{pp} voltage, which is higher than V_{DD} , is used to fully switch on bit line isolation during a restore portion of a read operation, as disclosed in the original '662 patent at column 5, lines 63-68, rather than providing a voltage through a

capacitive boosting operation.

8. All errors being corrected in the reissue application, including all amendments up to the time of and with the filing of this Declaration, arose without any deceptive intention on the part of the applicants.
9. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Name: Richard C. Foss

Signature: 

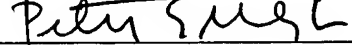
Date: 30/1/02

Residence: 28 Raith Gardens
Kirkcaldy, Fife KY2 5NJ Scotland

Citizenship: Canadian

Post Office Address: 28 Raith Gardens
Kirkcaldy, Fife KY2 5NJ Scotland

Name: Peter B. Gillingham

Signature: 

Date: 6 February 2002

Residence: 43 Slade Crescent
Kanata, Ontario, Canada K2K 2K9

Citizenship: Canadian

Post Office Address: 43 Slade Crescent
Kanata, Ontario, Canada K2K 2K9

Name: Robert Harland

Signature: *RH*

Date: 17 APRIL 02

Residence: Carp, Ontario K0A 1L0, Canada

Citizenship: Canadian

Post Office Address: Post Office Box 314
Carp, Ontario K0A 1L0 Canada

Name: Masami Mitsuhashi

Signature: *Masami Mitsuhashi*

Date: February 14, 2002

Residence: 8-20 2-choume 4-jou Ainosato
Kita-Ku Sapporo Hokkaido
Japan 002-8074

Citizenship: Japanese

Post Office Address: 8-20 2-choume 4-jou Ainosato
Kita-Ku Sapporo Hokkaido
Japan 002-8074

Name: Atsushi Wada

Signature: Atsushi Wada

Date: 28th Feb. / 2002

Residence: 1-30-3 Mangoku
Ogaki, Gifu
503 Japan

Citizenship: Japanese

Post Office Address: 1-30-3 Mangoku
Ogaki, Gifu
503 Japan

CERTIFICATE UNDER 37 C.F.R. § 3.73(b)

Applicant(s): Richard C. Foss, Peter B. Gillingham, Robert Harland, Masami Mitsuhashi and Atsushi Wada

Application No.: 10/032,431 Filed: December 21, 2001

For: DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT ISOLATING TRANSISTORS

MOSAID TECHNOLOGIES INCORPORATED, a corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

certifies that it is

- A. ☒ the assignee of the entire right, title and interest in the patent application identified above.
- B. ☐ an assignee together with [] of the entire right, title and interest in the patent application identified above. A separate Certificate under 37 CFR § 3.73(b) is attached.

The right, title and interest of the above-named assignee in the patent application identified above is established by virtue of:

- A. ☐ An assignment from the inventor(s) of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

- B. ☒ A chain of title from the inventor(s) of the patent application identified above, to the current assignee as shown below:

1. From: Richard C. Foss, Peter B. Gillingham, Robert Harland, Masami Mitsuhashi and Atsushi Wada
To: Mosaid, Inc.
The document was recorded in the Patent and Trademark Office at
Reel 6475, Frame 170-171, or for which a copy thereof is attached.
2. From: Mosaid, Inc. To: Mosaid Technologies, Inc.
The document was recorded in the Patent and Trademark Office at
Reel 7185, Frame 0598-0602, or for which a copy thereof is attached.
3. From: _____ To: _____
The document was recorded in the Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are attached.

☐ Copies of assignments or other documents in the chain of title are attached.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: 6 February 2002

Name: Peter Gillingham

Title: Vice President Business Development

Signature: *Peter Gillingham*